

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	303	345/537.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L2	138	345/539.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L3	529	345/536.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L4	268	345/530.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L5	472	345/545.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L6	205	345/559.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L7	166	345/556.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L8	246	345/519.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L9	33266	double same buffer\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L10	5747	double adj2 buffer\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20

L11	56962	(off adj2 chip external) near2 (memory buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L12	34603	display adj2 controller\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L13	24355	frame adj2 buffer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L14	18622	status adj2 register\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L15	5348	double adj buffer\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L16	27496	display adj controller\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L17	20804	frame adj buffer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L18	38691	graphic\$1 and (process\$3 accelerat\$3 generat\$3) and chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 16:20
L19	1297	L15 and L11 and chip	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20

L20	149	L19 and L16	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L21	132	L20 and cop\$4	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L22	125	L20 and copy\$3	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L23	119	L22 and L17	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L24	119	L23 and register\$1	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L25	110	L23 and register\$1 same display	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L26	42	L15 same L16	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L27	98	L14 and L15 and L12	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L28	327	graphic\$1 adj (process\$3 accelerat\$3 generat\$3) adj2 chip	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L29	241	graphic\$1 adj (processor accelerator generator) adj2 chip	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L30	164	L29 and display same control\$4	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20
L31	62	L29 and display near2 control\$4	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/01/27 16:20